

## ΔΗΜΟΣΙΕΥΣΕΙΣ – Δημήτριος Μετάφας

### Δημοσιεύσεις σε διεθνή περιοδικά:

- J1. O. Koufopavlou, **D. E. Metafas**, and C. E. Goutis "Architectures and VLSI module generator for expressing digital signals in decibels," *International Journal of Electronics*, Vol. 71, No. 2, pp. 297-307, 1991.
- J2. **D. E. Metafas** and C. E. Goutis "A DSP processor with a powerful set of elementary arithmetic operations based on CORDIC and CCM algorithms," *Journal of Microprocessing and Microprogramming*, No. 30, pp. 51-58, 1990.
- J3. **D. E. Metafas**, S. S. Nikolaidis, and C. E. Goutis "Real-time Cepstrum computation based on an advanced CORDIC processor", *Journal of Microprocessing and Microprogramming*, No. 37, pp. 57-60, 1993
- J4. **D. E. Metafas** and C. E. Goutis "A floating-point advanced CORDIC processor," *Journal of VLSI and Signal Processing (Elsevier)*, 10 (1995), pp. 53-65, 1995.
- J5. **D. E. Metafas**, E. Mariatos, S. S. Nikolaidis, and C. E. Goutis "Implementation of Given's Rotation processors for DSP real-time applications," *Journal of Microprocessing and Microprogramming*, No. 38, pp. 351-357, 1993.
- J6. S. L. Horianopoulos, **D. E. Metafas**, C. E. Goutis, and T. Deliyannis "A VLSI Synthesis Tool for complementary output delta modulation FIR filters," *Journal of Microprocessing and Microprogramming*, No. 34, pp. 139-142, 1992.
- J7. G. P. Economou, S. S. Nikolaidis, **D. E. Metafas**, and C. E. Goutis "Development of a Technology Independent Library for ASIC design," *Journal of Microprocessing and Microprogramming*, No. 39, pp.241-244, 1993.
- J8. L. Bisdounis, **D. E. Metafas**, A. M. Maras, C. Mavridis "VLSI Implementation of Digit-Serial Arithmetic Modules," *Journal of Microprocessing and Microprogramming*, No. 39, pp. 251-254, 1993.
- J9. H. C. Karathanasis, C. N. Dre, **D. E. Metafas** and S. V. Blionas "Designing a DSP for DECT and GSM/DCS1800 baseband processing," *Real Time magazine*, 96/3, pp. 22-31, 1996.
- J10. C. Drosos, M. Zayadine, **D. Metafas**, and H. Thielemans, "Embedded real-time communication protocol development using SDL for ARM microprocessor," *Dedicated Systems Magazine*, Jan. 2001.
- J11. S. Blionas, K. Masselos, C. Dre, C. Drosos, F. Ieromnimon, **D. Metafas**, T. Pagonis, A. Pnematikakis, A. Tatsaki, T. Trimis, A. Vontzalidis "Prototyping of a 5 GHz WLAN Reconfigurable System-on-Chip," *IEICE Trans. Inf. & Syst.*, Vol.E86-D, No. 5 May 2003, pp. 901-909.
- J12. L. Bisdounis, C. Dre, S. Blionas, **D. Metafas**, A. Tatsaki, F. Ieromnimon, E. Macii, Ph. Rouser, R. Zafalon and L. Benini, "Low-Power system-on-chip architecture for Wireless LANs," *IEE Proc. Computers and Digital Techniques*, vol. 151, no. 1, pp. 2-15, Jan. 2004.
- J13. C. Drosos, **D. Metafas**, S. Blionas, and G. Papadopoulos, "Rapid prototyping of a wireless LAN implementation using a UML-based system design methodology," *IEICE Trans. Inf. & Syst.*, Vol. E87-D, No.8, Aug. 2004, pp. 2058-2069.
- J14. C. Drosos, L. Bisdounis, **D. Metafas**, S. Blionas, A. Tatsaki, G. Papadopoulos, "Hardware-software design and validation framework for wireless LAN modems," *IEE Proceedings Computers and Digital Techniques*, vol. 151, no. 3, pp. 173-182, May 2004.

J15. C. Drosos, C. Dre, **D. Metafas**, D. Soudris, S. Blionas, "The low power analogue and digital baseband processing parts of a novel multimode DECT/GSM/DCS1800 terminal," *Microelectronics Journal* (Elsevier), Vol. 35, Issue 7, pp. 609–620, July 2004.

#### **Κεφάλαια βιβλίων:**

B1. H. C. Karathanasis, C. N. Dre, **D. E. Metafas** and S. V. Blionas "On the design of a Baseband Processor for DECT and GSM/DCS1800," *Embedded Microprocessor Systems* (Book), pp. 453-462, IOS Press, 1996.

#### **Δημοσιεύσεις σε διεθνή συνέδρια:**

C1. **D. E. Metafas** and C. E. Goutis "A floating point pipeline CORDIC processor with extended operation set," *Proc. of IEEE International Symposium on Circuits and Systems (ISCAS)* 1991, pp. 3066-3069, 1991.

C2. **D. E. Metafas**, G. Krikis, and C. E. Goutis "VLSI design of an 8-bit fixed-point CORDIC processor with extended operation set," *Proc. of EUROASIC Conference* 1991, pp. 158-161, 1991.

C3. D. Soudris, M. Birbas, O. Koufopavlou, **D. Metafas**, S. Nikolaidis, C. Goutis, and S. Theodoridis "All-digital spectrum analyser based on a parallel algorithm," *Proc. MELECON Conference* 1991, pp. 408-411, 1991.

C4. S. L. Horianopoulos, **D. E. Metafas**, C. E. Goutis, and T. Deliyannis "A Delta modulation FIR filter High Level Synthesis Tool," *Proc. of Fourth ISMM International Conference on Parallel and Distributed Computing and Systems (PDCS)* 1991, pp. 95-99, 1991.

C5. S. S. Nikolaidis, **D. E. Metafas**, and C. E. Goutis "CORDIC based pipeline architecture of all-pass filters," *Proc. of IEEE International Symposium on Circuits and Systems (ISCAS)*, pp. 1917-1920, 1993.

C6. E. P. Mariatos, **D. E. Metafas**, J. A. Hallas and C. E. Goutis "A fast DCT processor based on special purpose CORDIC rotators," *Proc. of IEEE International Symposium on Circuits and Systems (ISCAS)* 1994, pp. 271-274, 1994.

C7. **D. E. Metafas**, H. C. Karathanasis and S. V. Blionas "Industrial approach in design methodologies for mobile communications systems," *Proceedings 7th IEEE Int. Workshop on Rapid System Prototyping*, pp. 122-126, 1996.

C8. F. Ieromnimon, C. Dre, **D. Metafas**, C. Drosos, V. Koratzinos, A. Alexopoulou, S. Blionas, "On the Integration of Diverse Testing Strategies in a Low-Power Processor," *Design, Automation and Test in Europe Conference 2000, (DATE 2000)*, Paris, France, March 2000.

C9. C. Drosos, M. Zayadine and **D. Metafas**, "Real-Time Communication Protocol Development Using SDL for an Embedded System On Chip Based on ARM Microcontroller," *Proc. 13th Euromicro Conference on Real-Time Systems*, pp. 89-94, Delft, Netherlands, June 2001.

C10. C. Drosos, C. Dre, **D. Metafas**, D. Soudris and S. Blionas, "The Low Power Baseband Processing Parts of a Novel Dual Mode Dect/Gsm Terminal," *8th IEEE International Conference on Electronics, Circuits and Systems (ICECS 2001)*, Malta, September 2001.

C11. S. Blionas, K. Masselos, C. Dre, C. Drosos, F. Ieromnimon, T. Pagonis, A. Pnematikakis, A. Tatsaki, T. Trimis, A. Vontzalidis and **D. Metafas** "A HIPERLAN/2 - IEEE 802.11a Reconfigurable System-on-Chip," 2002 International Conference on Field Programmable Logic and Applications, (Montpellier, France), Lecture Notes on Computer Science 2438, Springer-Verlag, Berlin Heidelberg, pp. 1080-1083, September 2002.

C12. S. Blionas, K. Masselos, C. Dre, F. Ieromnimon, T. Pagonis, A. Pnematikakis, A. Tatsaki, T. Trimis, A. Vontzalidis and **D. Metafas**, "Design Story: A Hiperlan2/IEEE802.11x Reconfigurable SoC for indoor WLANs and outdoor wireless links. A pilot project for the future generation configurable wireless communications products," Workshop on Heterogeneous Reconfigurable Systems on Chip, Chances, Application, Trends, April 2002.

C13. C. Drosos, L. Bisdounis, **D. Metafas**, S. Blionas, A. Tatsaki, "A Multi-Level Validation Methodology for Wireless Network Applications," Integrated Circuit and System Design: Power and Timing Modeling, Optimization and Simulation (Book, proceedings of 14<sup>th</sup> international workshop on Power and Timing Optimization and Simulation, PATMOS 2004), pp. 332–341, Springer, 2004

C14. C. Drosos, **D. Metafas** and G. Papadopoulos, "A UML-Based Methodology for the System Design of a Wireless LAN Prototype," 7<sup>th</sup> IEEE International Symposium on Object-oriented Real-time distributed Computing (ISORC 2004), pp. 45-51, May 2004.

C15. V. Menkovski, **D. Metafas**, "AI Model for Computer games based on CBR and AI planning," 3rd International Conf. on Digital Interactive Media in Entertainment and Arts (DIMEA 2008), pp. 295-302, Sep. 2008.

C16. Th. Karachristos, D. Apostolatos, **D. Metafas**, "A Real-Time Streaming Games-on-Demand System," 3rd International Conf. on Digital Interactive Media in Entertainment and Arts (DIMEA 2008), pp. 51-56, Sep. 2008.

C17. S.S. Nikolaidis, **D.E. Metafas** "CORDIC versus conventional logic for realisation of normalised lattice all-pass filters", IEEE Conf. on Electronics, Circuits, and Systems, 1996. ICECS'96, pp. 538-541, Oct. 1996

C18. V. Choleva, L. Koutsikos, S. Zourelidis, V. Filis, **D. Metafas**, C. Patrikakis "Safer Internet: Enhancing Good Practices on the Internet through Games Based Learning for Greek Elementary School Students", 7th European Conference on Games Based Learning ECGBL 2013, Oct. 2013 Porto, Portugal.

#### **Παρουσιάσεις σε διεθνή συνέδρια:**

CP01. T. Papateodoru, **D. Metafas**, B. Meparishvili, G. Goderdzishvili, "A Virtual Processor for the Security of MSP430 Equipped Sensor Boards", International Scientific Conference eRA-7, Athens, Sep. 2012

CP02. D. Goustouridis, E. Kyriakis-Bitaros, **D. Metafas**, B. Meparishvili, G. Goderdzishvili, "Non-linear methods applied in sensor networks for environmental monitoring", International Scientific Conference eRA-7, Athens, Sep. 2012

CP03. **D. Metafas**, M. Rangoussi, B. Meparishvili, G. Goderdzishvili "Dynamic Applications on Complex Distributed Systems and Machine Learning algorithms", International Conference on Nanosensory Systems and Quantum Sensory Materials", Georgia, Jun. 2013

## Παρουσιάσεις σε εθνικά συνέδρια

1. L.Toumanidis, A.Charitopoulos, D.Metafas, **M.Rangoussi**, “E-advisor and E-evaluator Module developments for the Moodle E-learning platform based on ID3 classification algorithms”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.9)
2. E.Lignou, D.Metafas, **M.Rangoussi**, “Teaching Mathematics in high school: Content development in the Open Source E-Learning Platform Moodle”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.1)
3. A.Liapi, D.Metafas, **M.Rangoussi**, “A walk in Space: A case study for Early Age Education content developed in Moodle”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.2)
4. N.Gemistou, D.Metafas, **M.Rangoussi**, “An elementary school history course developed in Moodle: Alexander the Great and the development of Macedonian State”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.3)
5. N.Mpalatsouka, D.Metafas, **M.Rangoussi**, “Cultural and linguistic activities in High School education: development of an educational electronic course”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.4)
6. I.Komi, D.Metafas, **M.Rangoussi**, “Teaching a course on Computers in high school: Content development in the Open Source E-Learning Platform Moodle”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.5)
7. F.Peraki, D.Metafas, **M.Rangoussi**, “E-learning Platform support for a high school project on Shakespeare's Language”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.6)
8. M.Grigoraki, D.Metafas, **M.Rangoussi**, “Developing Content for elementary school students in the Open E-Learning Platform Moodle: A project on mythology”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.7)
9. A.Politi, D.Metafas, **M.Rangoussi**, “Halloween: enhancing intercultural understanding using the Open E-Learning Platform Moodle”, eRA-8 International Scientific Conference, TEI Piraeus, Greece, Sept. 23-25, 2013 (paper no. BB.3.13)

**Αναφορές (σύμφωνα με το Google Scholar):** Citations 139, h-index 8

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